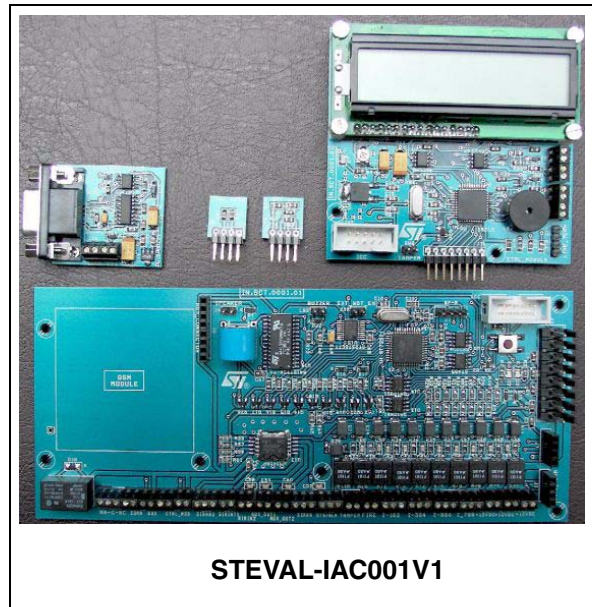


### Features

- 8 configurable, hard-wired inputs with programmable balance control
- 7 configurable, hard-wired outputs that can be associated with each input (4 SSR, 2 transistorized, and 1 relay)
- Alarm-on and CU-active output indicators
- 16 configurable partitions for inputs/outputs
- Expandable inputs and outputs with the I/O Expander modules connected to the bus
- Balanced and programmable tamper line (currently not managed by firmware)
- Programmable fire/smoke sensor inputs
- Mains loss and low battery indicators (currently not managed by firmware)
- Test mode for inputs/outputs
- Timer events for inputs/outputs
- Date and time stamp for up to 999 log events
- Up to 8 users with programmable access code for all partition combinations
- Encrypted communication bus for I/O expansion and system management
- Three-level password access control
- Protected bus installation and setup mode for additional security
- PC interface directly connected via the communication bus with user-friendly GUI

### Description

The central unit for the alarm system evaluation board provides a complete reference system for addressing the low to mid-range security sectors. The tool includes all the necessary hardware and software for the development of a complete alarm platform based on a bus network. In addition to a



control/programming panel connected through the bus, the GUI (graphical user interface) makes it simple to set up all control parameters. The entire system consists of four boards: the Central Unit board, control module board, Power supply board and the PC interface board. There is also PC GUI software that enables the user to easily configure all central unit parameters and to execute certain system tests. The system can be expanded by means of input/output expansion boards, as well as a GSM board (connector available) for managing the CU using a cell phone, and other modules using radio-frequency identification (RFID), smartcard or keyboard devices connected through the bus. For more information please refer to the document UM0272.

# 1 Board schematics

Figure 1. Central unit board layout

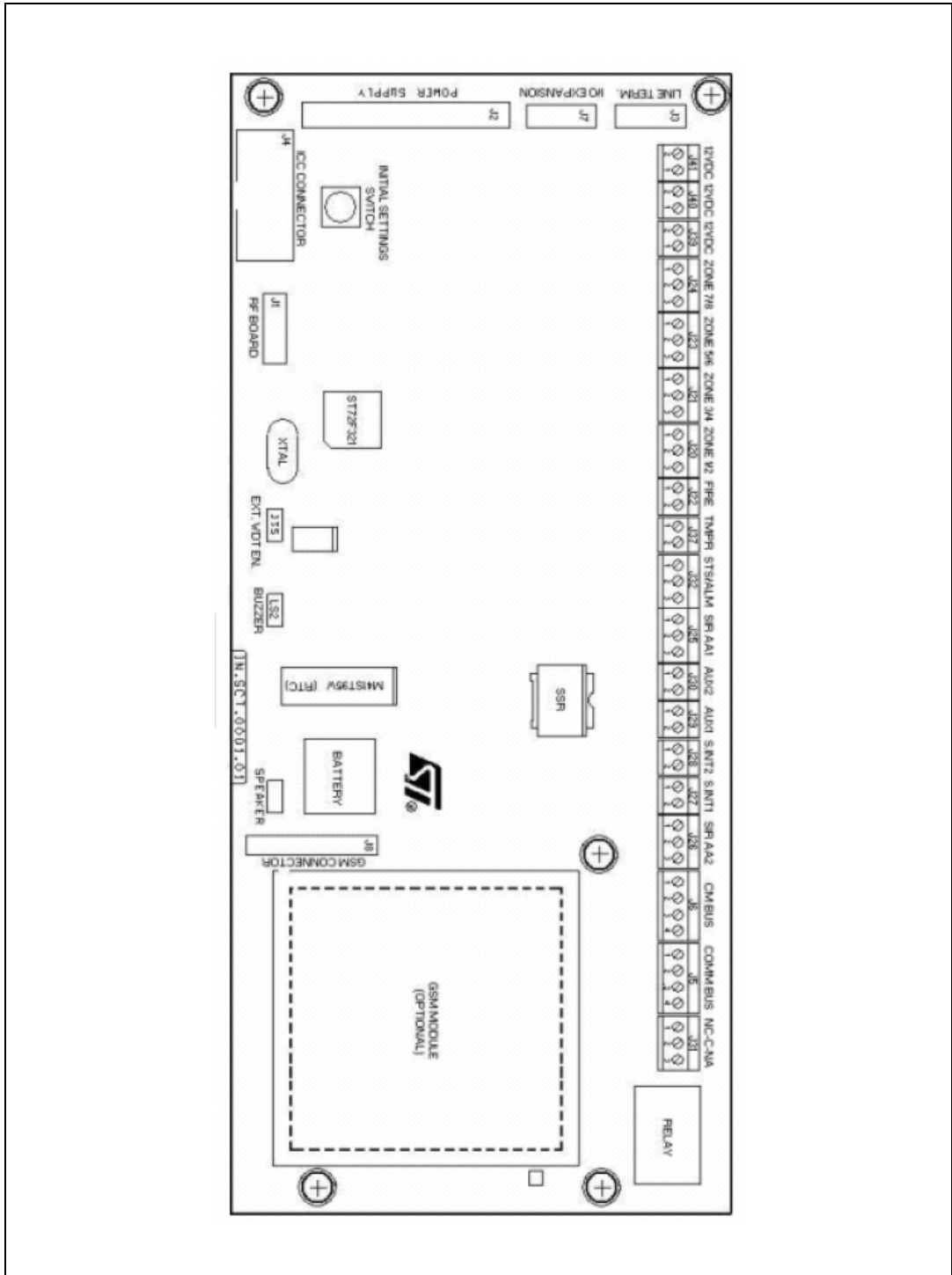


Figure 2. PC interface board layout

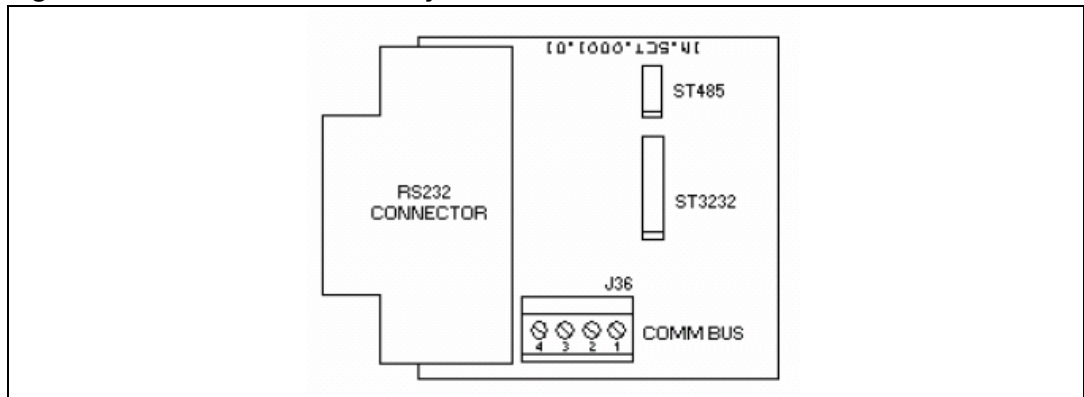


Figure 3. Control module board layout

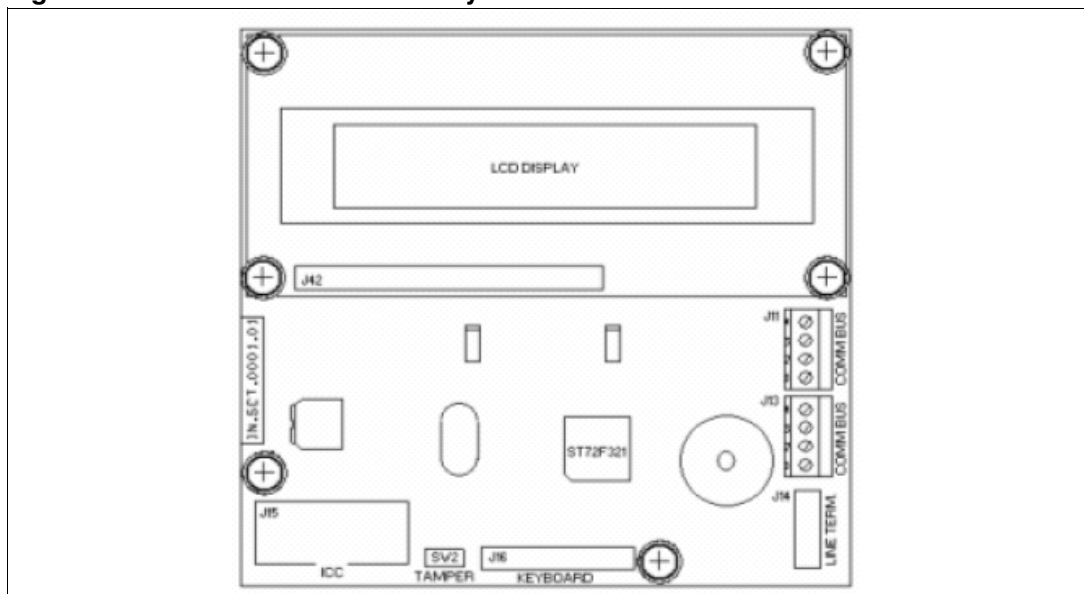
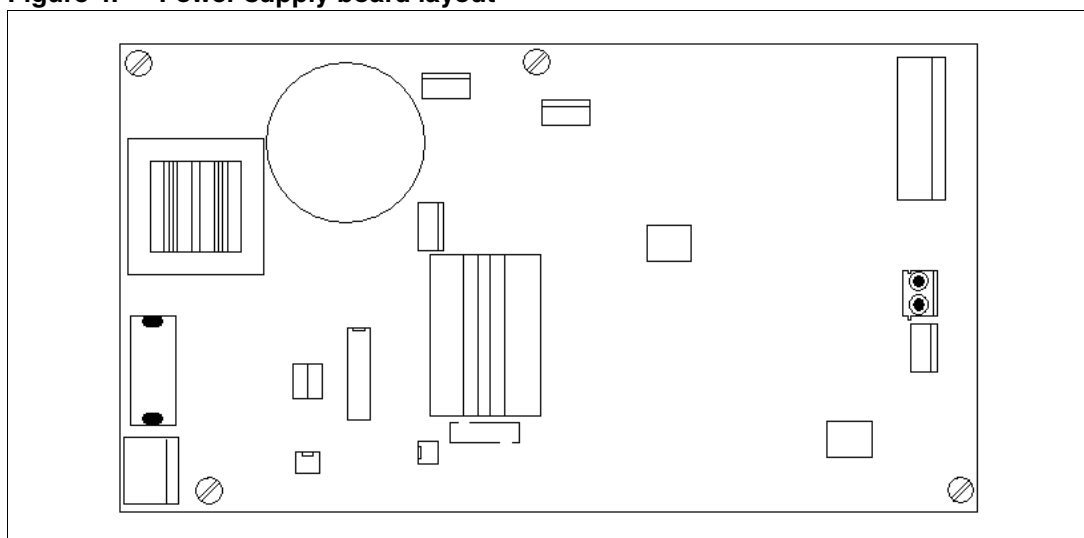


Figure 4. Power supply board layout



## 2 Revision history

**Table 1. Document revision history**

Date	Revision	Changes
22-Oct-2007	1	Initial release

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